

EXECUTIVE SUMMARY

Intel's Connected Devices Patent Auction

Overview

Earlier this year, Intel launched a Cellular and Connected Devices Patent Auction for the sale of two patent portfolios. Building upon Intel's recent sale of a large portfolio of cellular technology patents in connection with the sale of the majority of Intel's smartphone modem business, Intel is re-launching its auction process for the sale of the Connected Devices Portfolio, another rarely available patent portfolio. This offering is an opportunity for a buyer to obtain a treasure trove of high-quality patents regarding a wide array of key non-cellular technologies, from one of the world's most innovative companies.

❖ *Portfolio Description and Strength.*

- The Connected Devices Portfolio is a collection of 83 patent families, comprised of 436 patents and patent applications, focused on foundational technologies broadly applicable across the semiconductor and electronics industries, including computer architecture, process technology, packaging and networking.
- To date, 73 patents have been claim charted 84 times against specific implementations and standards adoption.
- Over 40 of the offered patents have been used in the § 102 rejection of third-party patents.
- Over 55 of the offered patents have been used as the primary § 103 reference in rejecting 160+ third-party patents.

❖ *Broad Industry Coverage.* As a result of its broad technological coverage, the Connected Devices Portfolio raises opportunities in a wide array of industries, including the smartphone industry (which had global sales of US\$552 billion in 2018) and the wireless semiconductor industry (which had global sales of US\$125.9 billion in 2018, excluding Intel).

❖ *Substantially Unencumbered.* The Connected Devices Portfolio is substantially unencumbered with respect to at least 75% of the addressable market for smartphones. The Connected Devices Portfolio is also substantially unencumbered with respect to 3 of the top 5 players in the wireless semiconductor market.¹

¹ Disclaimer: This presentation is provided to you for informational purposes only, and is not permitted to be shared outside of your organization, to facilitate your independent evaluation of whether to engage in the patent auction described herein and to potentially purchase the Connected Device Portfolio (the "Transaction").

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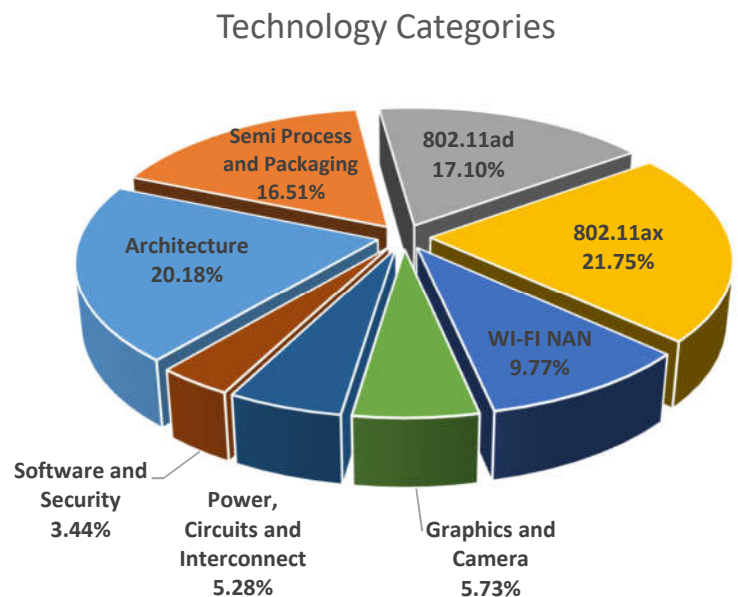
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Portfolio Details

Intel's vision of a connected world has led Intel to spearhead cutting-edge work across the technology stack of connected devices, from the manufacturing of subcomponents, up through software and services operated by connected devices. Through this work, Intel has built one of the world's most valuable and most comprehensive collections of connected device-related patents that are relevant across a variety of industries, and which benefit from decades of scientific innovation and expertise, and billions of dollars of investment in R&D.

The technologies covered by the Connected Devices Portfolio span the stack of non-cellular technologies in modern connected devices, such as smartphones. The following represents the wide array of technologies covered by the Connected Devices Portfolio:

- **Architecture:** instruction set architecture, artificial intelligence, hardware virtualization, and memory interface.
- **Semiconductor Process and Packaging:** transistor structures, lithography, metallization, die stacking, thru-silicon vias, and packaging and assembly.
- **Power Management, Circuits and Interconnect:** circuit blocks, digital logic, clock generation, and power delivery circuit solutions.
- **Wireless Connectivity:** components, subsystems, and protocols for non-cellular wireless (e.g., WiFi) communications and network management.
- **Graphics and Camera:** rendering, lighting/shading, ray tracing, compression and processing.
- **Software and Security:** system software, processing algorithms, data security.



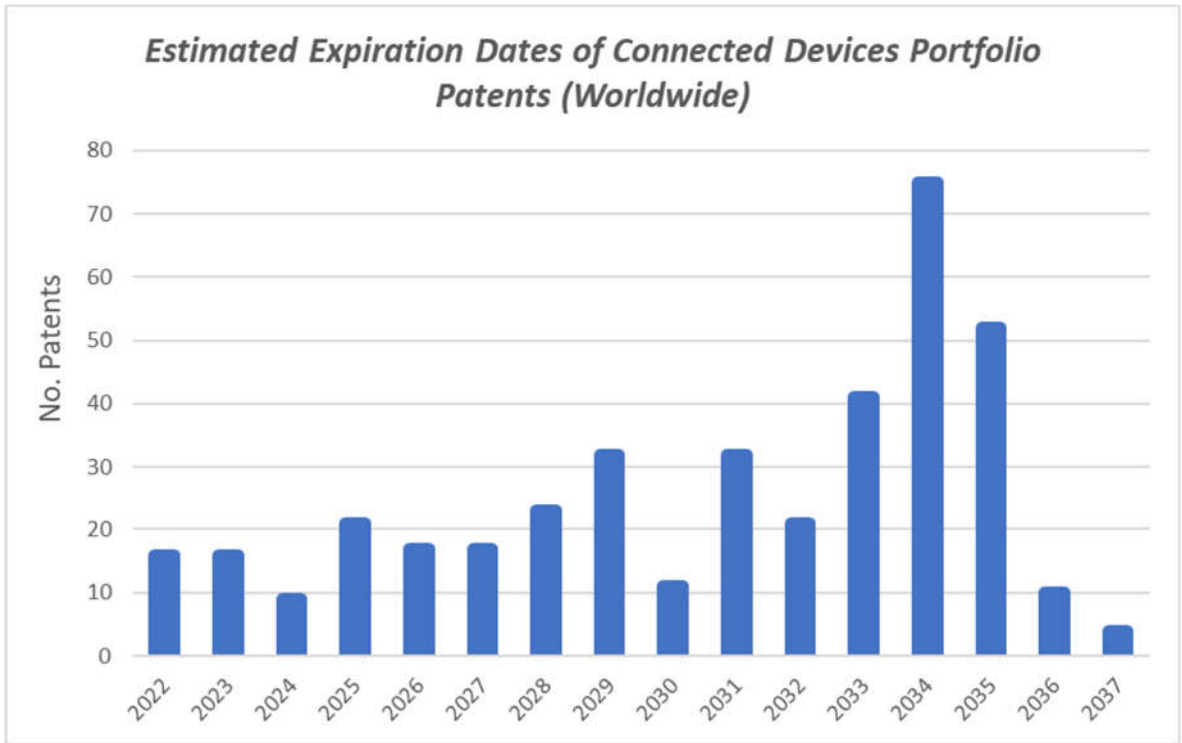
- ❖ *High Value; Evidence of Use.* The Connected Devices Portfolio includes over 80 claim charts, including:

Technology	Number of Claim Charts
Microprocessor Architecture	16
Semiconductor Process and Packaging	27
802.11ad	8
802.11ax	8
Wi-Fi NAN	5
Graphics and Camera	5
Power, Circuits and Interconnect	6
Software and Security	6

- ❖ *Broad Geographic Scope.* The Connected Devices Portfolio has broad geographic coverage, in top global technology markets:

Geography	Patents/Patent Applications
Americas:	
USA	162
Rest of Americas	8
Asia-Pacific:	
PRC	64
Taiwan	47
Japan	30
South Korea	20
Rest of APAC	26
Europe:	
EPO	22
Germany	21
Rest of Europe	36

- ❖ *Substantial Remaining Life.* The patents in the Connected Devices Portfolio have an average of over 12 years of estimated remaining life.



Auction Process

- Intel anticipates receiving non-binding indications of interest from bidders by March 31, 2020. Intel will then select one or more finalists, who will be invited to negotiate a definitive purchase agreement with Intel, with closing expected to take place by June 30, 2020.
- In order to obtain access to the data room (which includes detailed information regarding the portfolios), you are required to execute the Non-Disclosure Agreement. Please direct any questions regarding the auction or Non-Disclosure Agreement to Nader Mousavi of Sullivan & Cromwell (mousavin@sullcrom.com; (650) 461-5660).

Connected Devices Portfolio – Selected Featured Patents

The following is a sampling of the patents covered by the 84 high-quality claim charts included in this rare opportunity.

U.S. Patent Number	Title	Featured Claims ⁶
US8188792	TECHNIQUES FOR CURRENT MIRROR CIRCUITS	1, 2, 3, 5, 6, 7
US10200514	PRE-HIGH-EFFICIENCY (HE)-SHORT TRAINING FIELD PREAMBLE TRANSMISSION FOR THE HE-TRIGGER BASED PHYSICAL LAYER CONVERGENCE PROTOCOL (PLCP) PROTOCOL DATA UNIT (PPDU)	13
US7363474	METHOD AND APPARATUS FOR SUSPENDING EXECUTION OF A THREAD UNTIL A SPECIFIED MEMORY ACCESS OCCURS	1, 3, 4
US8618609	APPARATUS FOR IMPROVING MULTI-GATE DEVICE PERFORMANCE	1, 3
US8402290	POWER MANAGEMENT FOR MULTIPLE PROCESSOR CORES	1, 5, 6, 7, 8
US8194687	ACCESS POINT CONFIGURED FOR STATION GROUP MANAGEMENT AND METHOD FOR MANAGING STATION-MANAGEMENT GROUPS	1
US9627321	METHODS AND APPARATUSES TO FORM SELF-ALIGNED CAPS	1, 2, 3
US8955039	MOBILE PLATFORM WITH SENSOR DATA SECURITY	7, 11, 12, 13
US7689046	HYBRID IMAGE AND MOVIE IMAGING SYSTEM AND METHOD	9
US7120755	TRANSFER OF CACHE LINES ON-CHIP BETWEEN PROCESSING CORES IN A MULTI-CORE SYSTEM	10
US7437581	METHOD AND APPARATUS FOR VARYING ENERGY PER INSTRUCTION ACCORDING TO THE AMOUNT OF AVAILABLE PARALLELISM	17, 22
US9262159	PERFORMING A CYCLIC REDUNDANCY CHECKSUM OPERATION RESPONSIVE TO A USER-LEVEL INSTRUCTION	1, 2, 3, 4, 5, 6, 7, 8, 9
US7709312	METHODS FOR INDUCING STRAIN IN NON-PLANAR TRANSISTOR STRUCTURES	1, 7, 8, 9, 12
US9035959	TECHNIQUE TO SHARE INFORMATION AMONG DIFFERENT CACHE COHERENCY DOMAINS	19, 20, 21
US9806095	HIGH VOLTAGE THREE-DIMENSIONAL DEVICES HAVING DIELECTRIC LINERS	1, 3, 4, 7, 9, 10, 11, 12, 14

⁶ Independent claims are bolded.